

GRAPHICS ACCELERATOR

Abstract of the Disclosure

5 A graphics display system integrated circuit processes analog
video input, digital video input, and graphics input. The system
incorporates a graphics accelerator that includes memory for
graphics data. The accelerator preferably includes a coprocessor
for performing vector type operations on a plurality of components
10 of one pixel of the graphics data. The accelerator also includes
an expanded instruction set for storing and loading data.

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